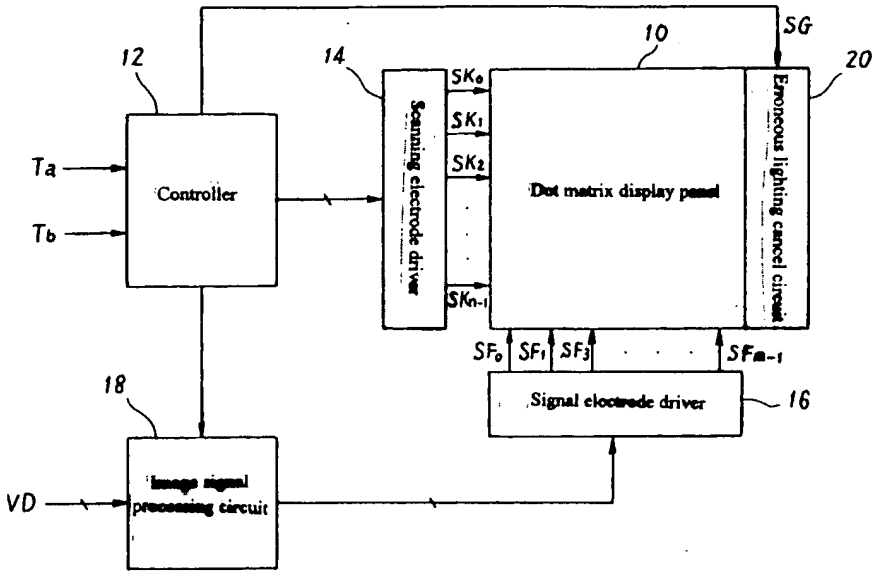

FIG. 1

Key:
10 Dot matrix display panel
12 Controller
14 Scanning electrode driver
16 Signal electrode driver
18 Image signal processing circuit
20 Erroneous lighting cancel circuit

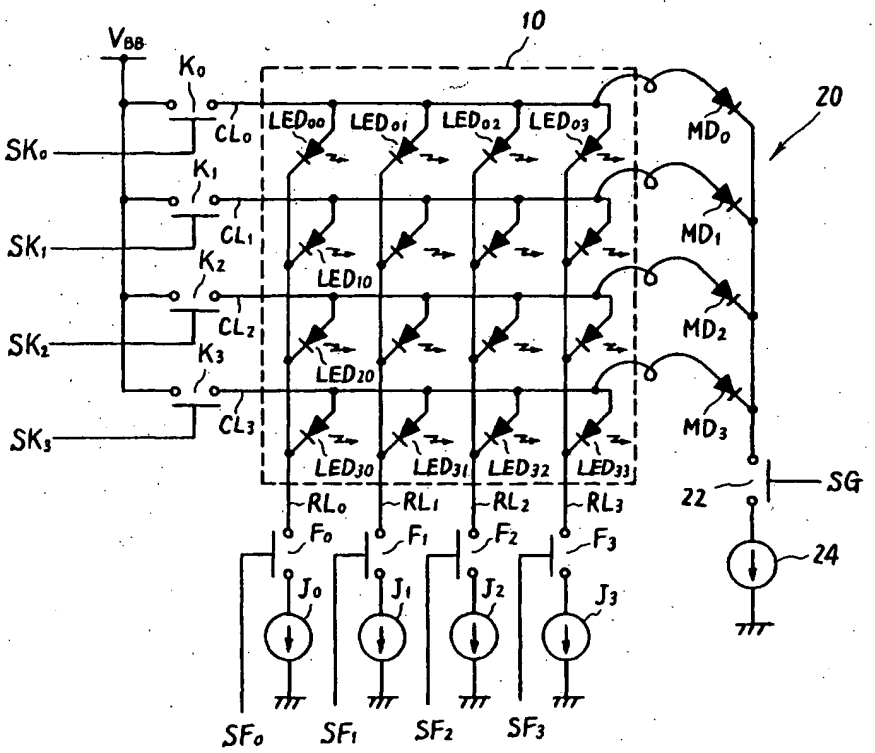
10
20
V_{BB}
K_0
K_1
K_2
K_3
CL_0
CL_1
CL_2
CL_3
SK_0
SK_1
SK_2
SK_3
LED_{00} LED_{01} LED_{02} LED_{03}
LED_{10}
LED_{20}
LED_{30} LED_{31} LED_{32} LED_{33}
MD_0
MD_1
MD_2
MD_3
RL_0 RL_1 RL_2 RL_3
F_0 F_1 F_2 F_3
J_0 J_1 J_2 J_3
22
SG
24
SF_0 SF_1 SF_2 SF_3

FIG. 2

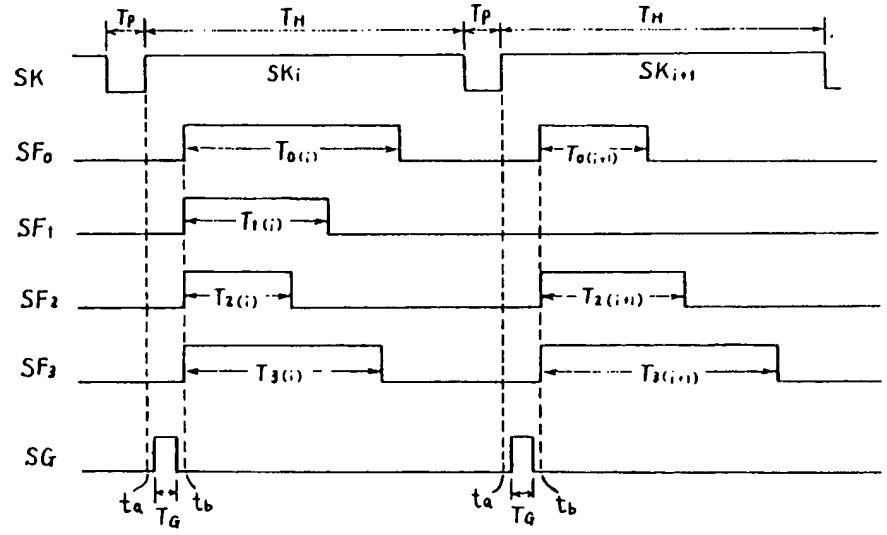
T_P
T_H
T_P
T_H
SK
SKi
SK_{i+1}
SF_0
SF_1
SF_2
SF_3
SG
$T_{0(G)}$
$T_{0(G+1)}$
$T_{1(G)}$
$T_{2(G)}$
$T_{2(G+1)}$
$T_{3(G)}$
$T_{3(G+1)}$
t_a
T_G
t_b
t_a
T_G
t_b

FIG. 3

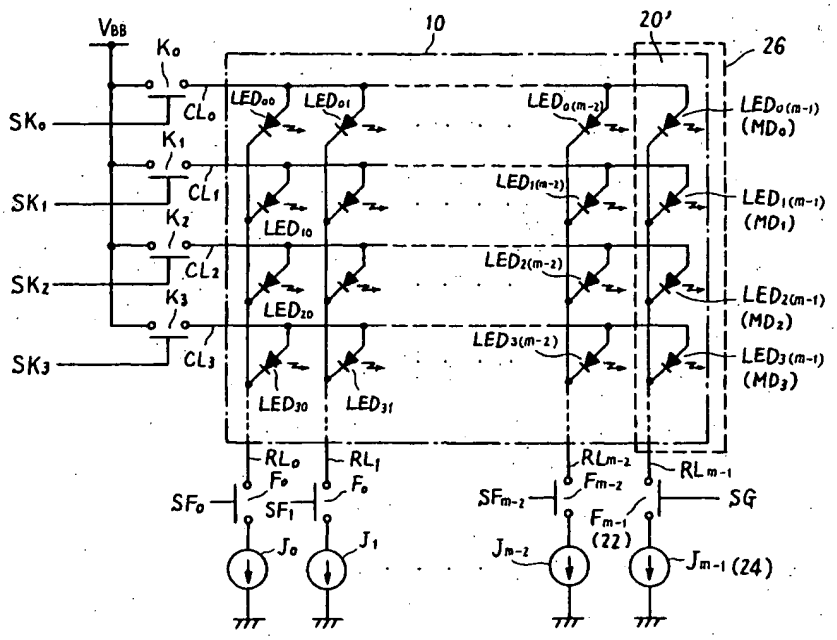
VBB
10
20'
26
K0
CL0
LED00
LED01
LED0(m-2)
LED0(m-1)
(MD0)
SK0
K1
CL1
LED1(m-2)
LED1(m-1)
(MD1)
SK1
LED10
K2
CL2
LED2(m-2)
LED2(m-1)
(MD2)
SK2
K3
CL3
LED3(m-2)
LED3(m-1)
(MD3)
SK3
LED30
LED31
RL0
RL1
RLm-2
RLm-1
F0
SF0
SF1
Fm-2
SFm-2
SG
Fm-1
(22)
J0
J1
Jm-2
Jm-1 (24)

FIG. 4

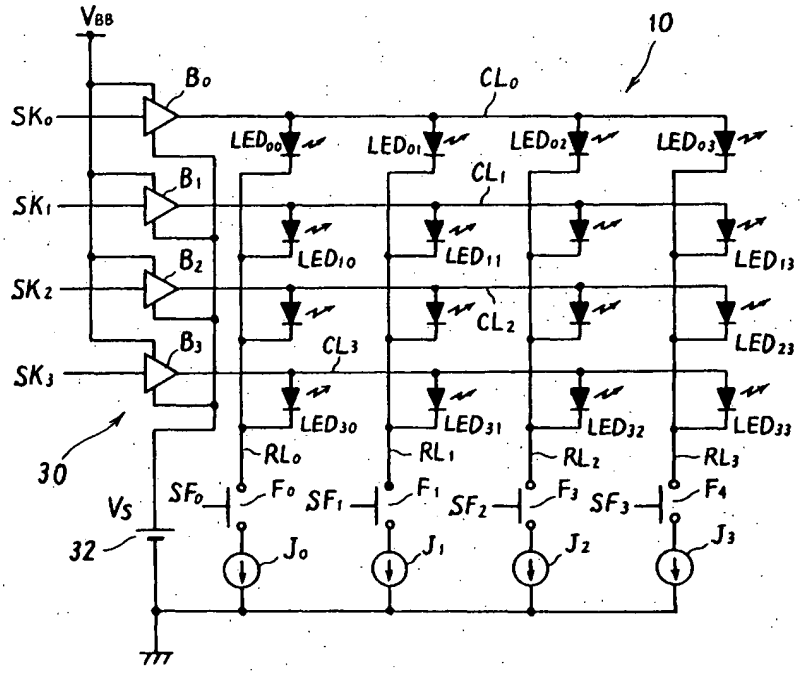
VBB
10
B0
CL0
SK0
LED00
LED01
LED02
LED03
CL1
B1
SK1
LED10
LED11
LED13
B2
SK2
CL2
LED23
CL3
B3
SK3
LED30
LED31
LED32
LED33
30
RL0
RL1
RL2
RL3
VS
SF0
F0
SF1
F1
SF2
F3
SF3
F4
32
J0
J1
J2
J3

FIG. 5

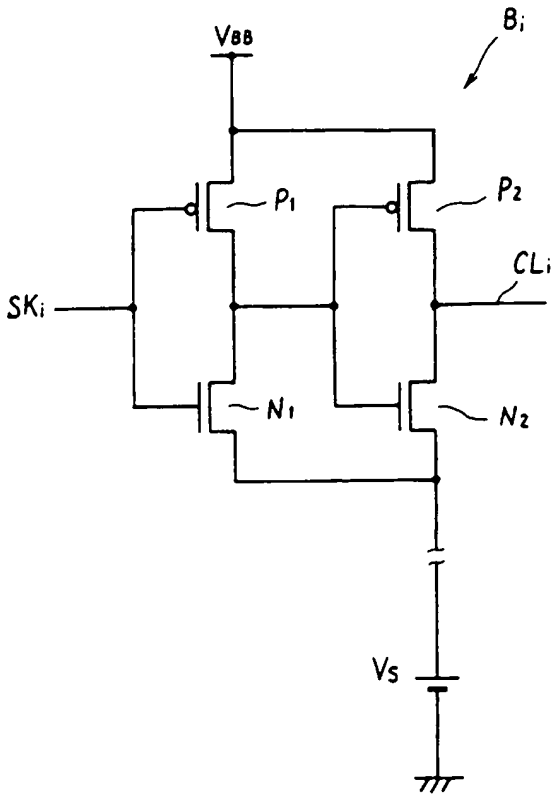

FIG. 6

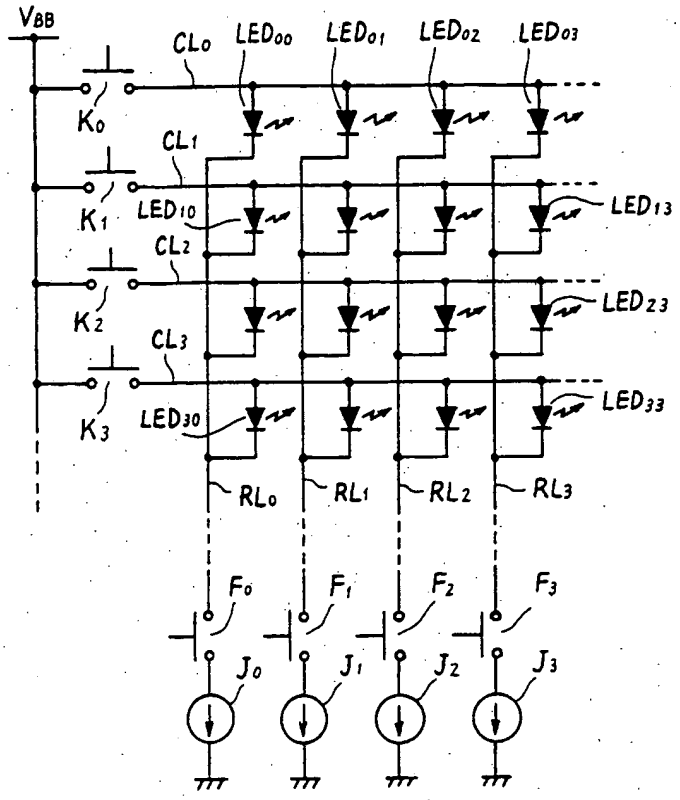
VBB
CL0
LED00
LED01
LED02
LED03
K0
CL1
K1
LED10
LED13
CL2
K2
LED23
CL3
K3
LED30
LED33
RL0
RL1
RL2
RL3
F0
F1
F2
F3
J0
J1
J2
J3

FIG. 7

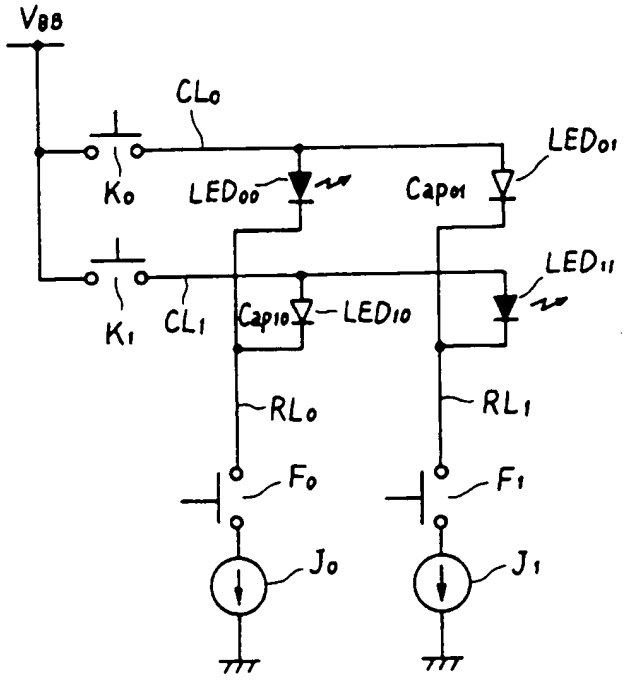
VBB
CL0
LED01
K0
LED00
Cap01
LED11
K1
CL1
Cap10
LED10
RL0
RL1
F0
F1
J0
J1

FIG. 8